## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE **PATENT** Date: August 29, 2002 plication No.: 09/779,096 Examiner: Ori Nadav Applicant: Shi-Tron LIN, et al. Group Art Unit 2811 Filed: 02/08/2001 Our Ref: B-4101 (618582-4/RPB:TGC For: "Output Buffer with Good ESD Protection" LETTER TO THE PATENT OFFICE OFFICIAL DRAFTSPERSON Hon. Commissioner of Patents and Trademarks Washington, DC 20231 Sir: The Examiner objected to the drawings of the above referenced application. The Applicant hereby submits photocopies of Figures 1A and 1B

The Examiner objected to the drawings of the above referenced application. The Applicant hereby submits photocopies of Figures 1A and 1B with the proposed corrections shown in red ink. Please approve the proposed corrections. Upon receipt of a Notice of Allowance, the Applicant will present formal drawings including these corrections.

I hereby certify that this correspondence is being deposited
with the United States Post Office with sufficient postage as
first class mail in an envelope addressed to: Commissioner of
Patents and Trademarks, Washington, D.C., 20231 on
August 29, 2002
(Date of Deposit)
Troy Guangyu Cai
(Name of Person Signing)
902
(Signature)
8/29/62
(Date)

Respectfully submitted,

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